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**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

APPL NUM 10035988	FILING DATE 12/24/2001	CLASS 711	SUBCLASS 305	GAU 2181 2185	EXAMINER <i>Richard Mason</i>
**APPLICANTS: Heynemann Tom; Sprouse Jeffrey; Knowles-Michael;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	20206-29 (P00-2952)
Verified and Acknowledged Examiners's initials			
TITLE : Method and apparatus for ensuring multi-threaded transaction ordering in a strongly ordered computer interconnect			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE			DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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